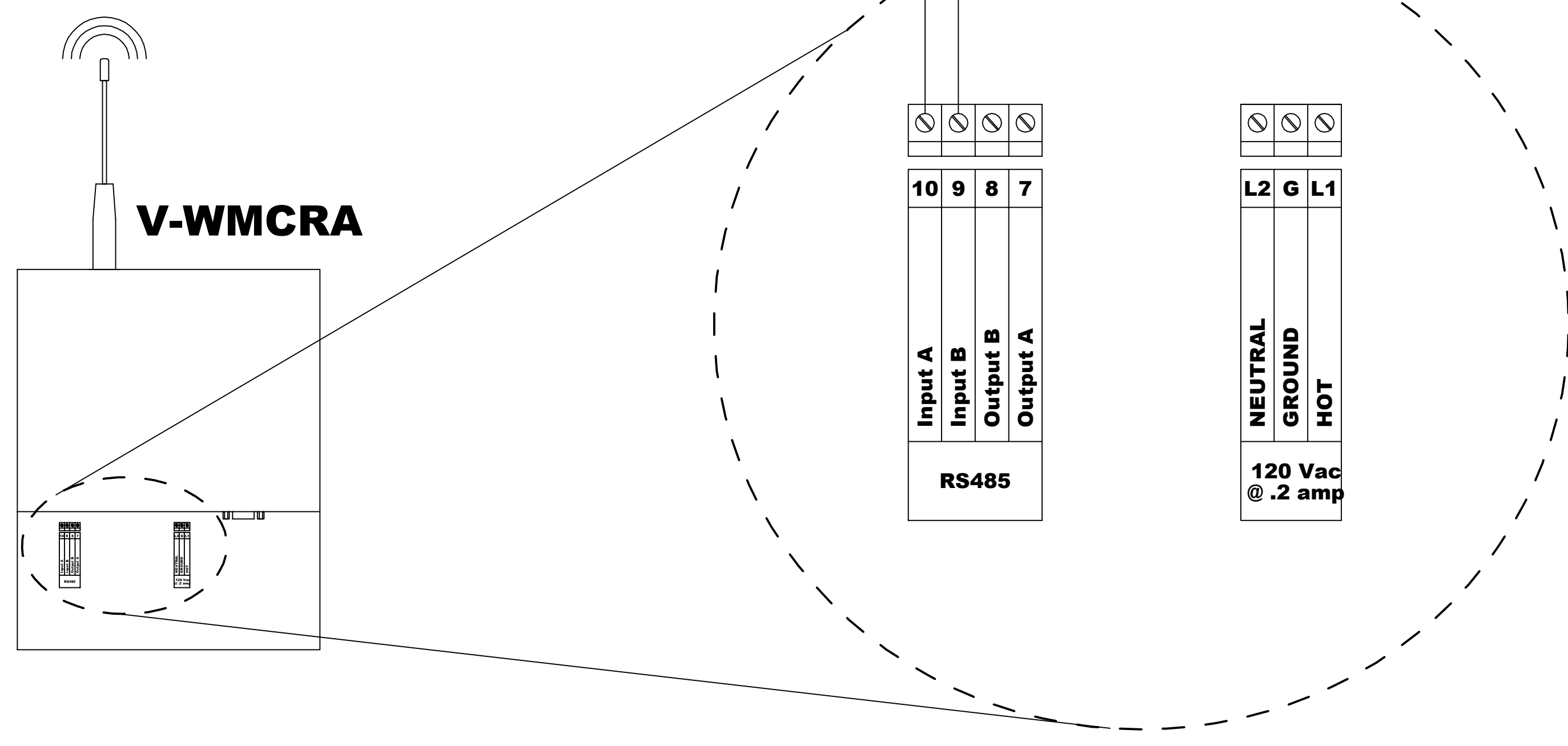
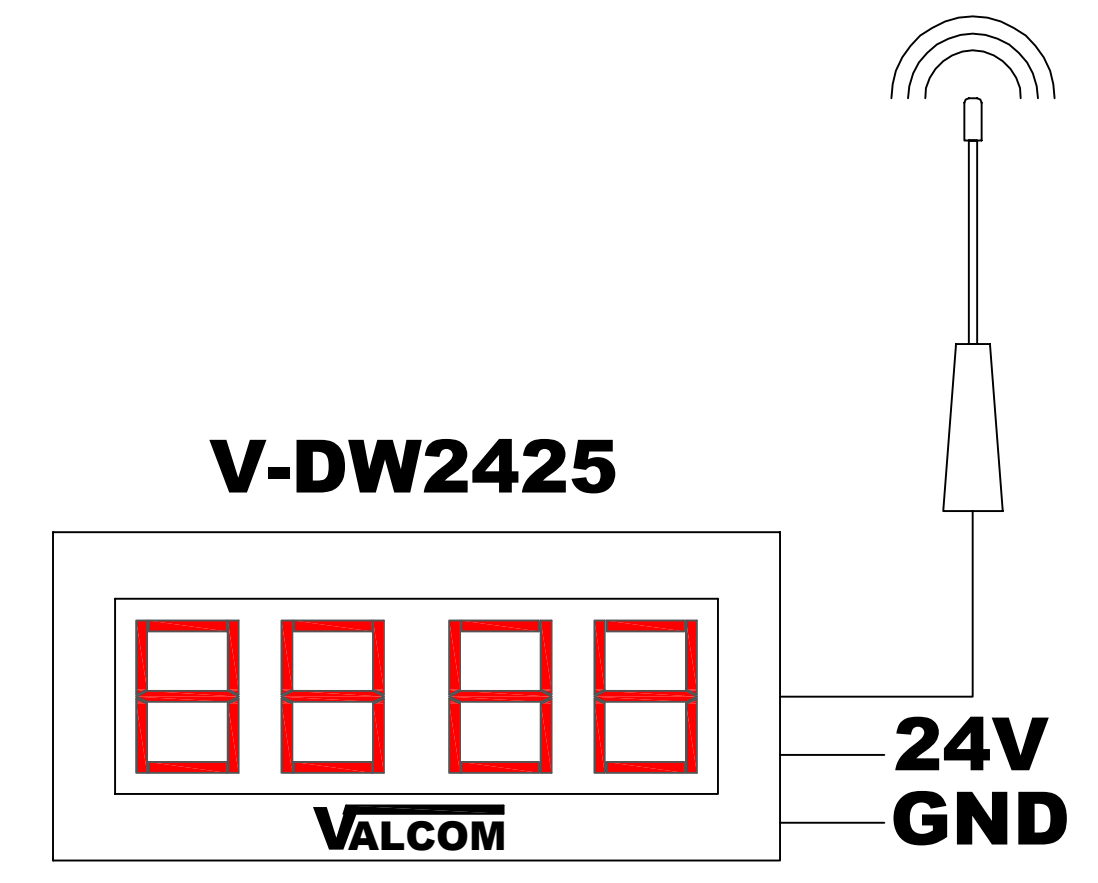
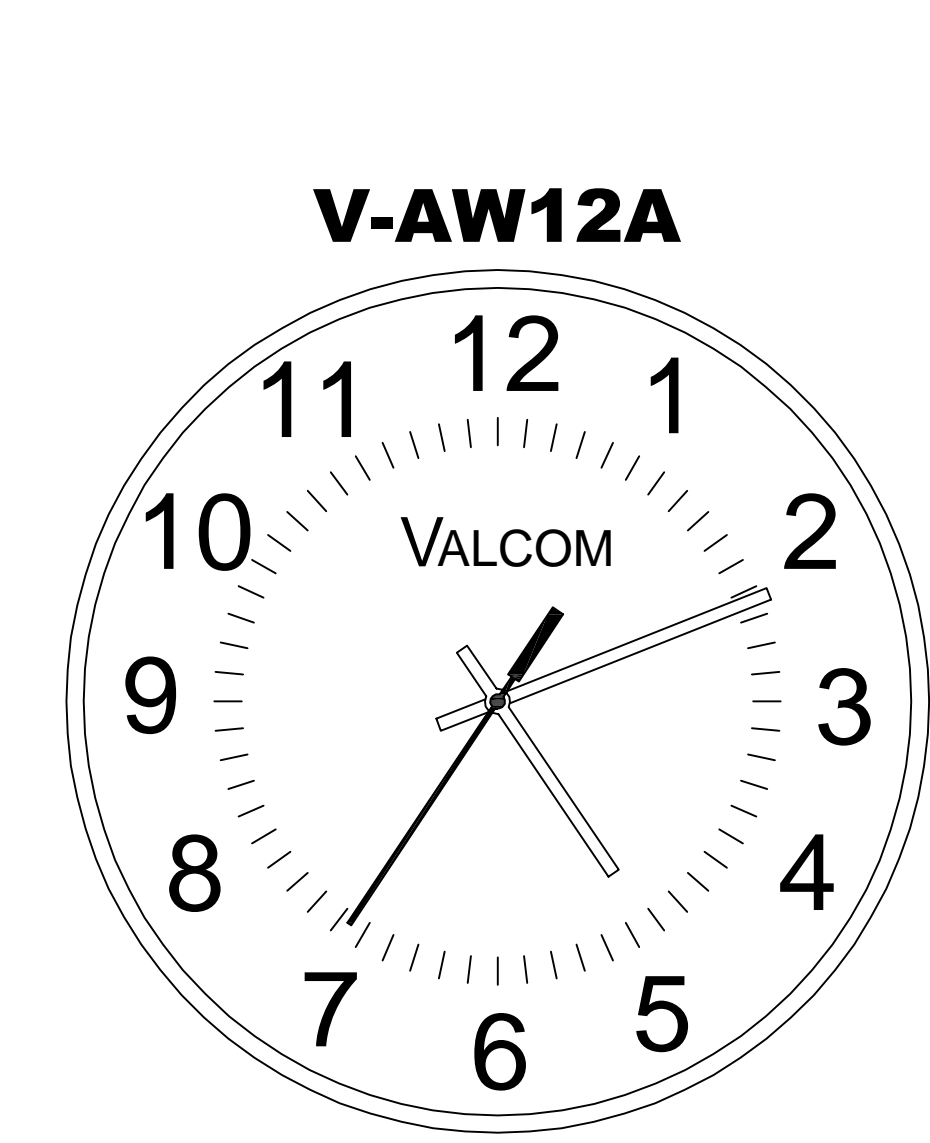
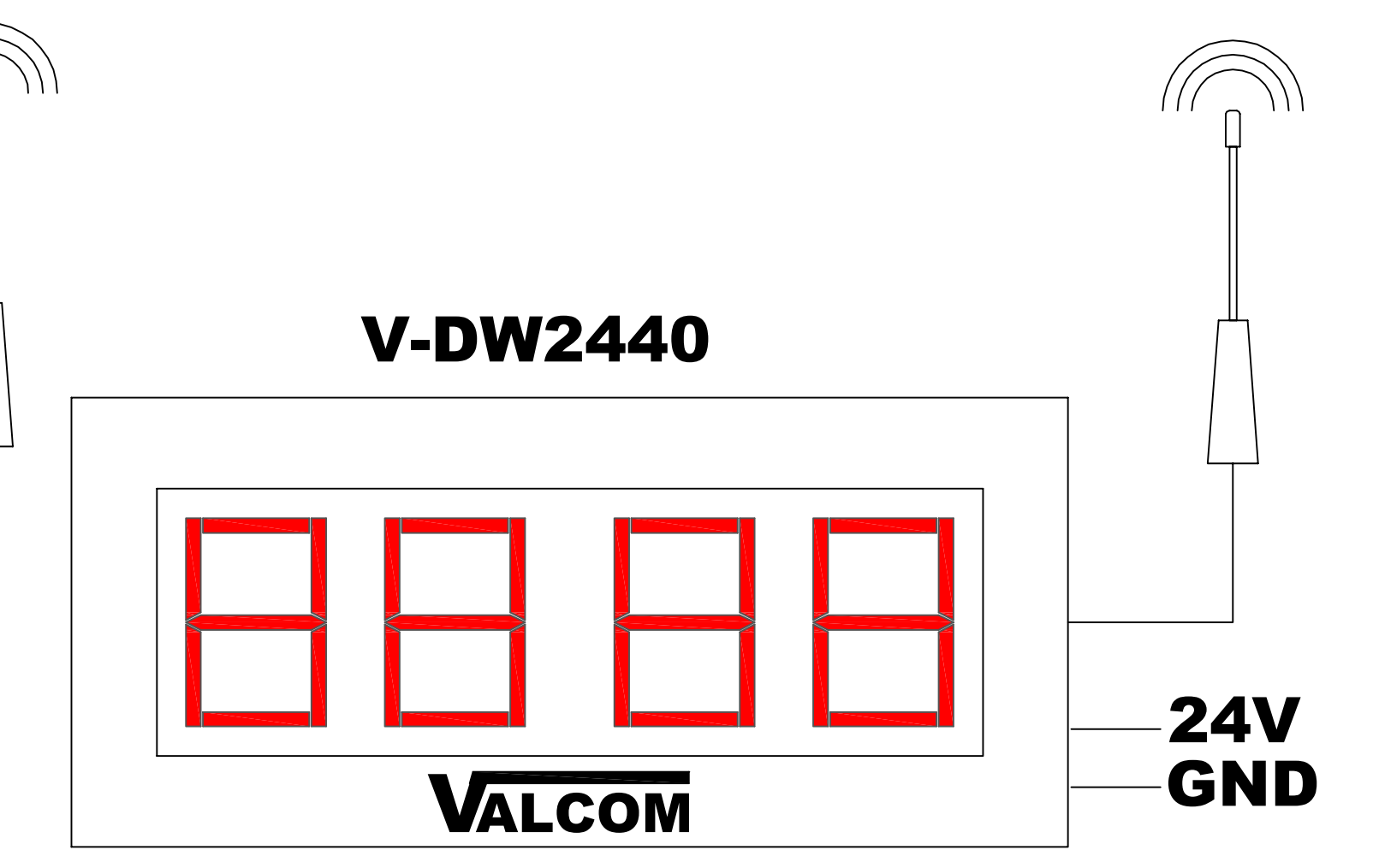
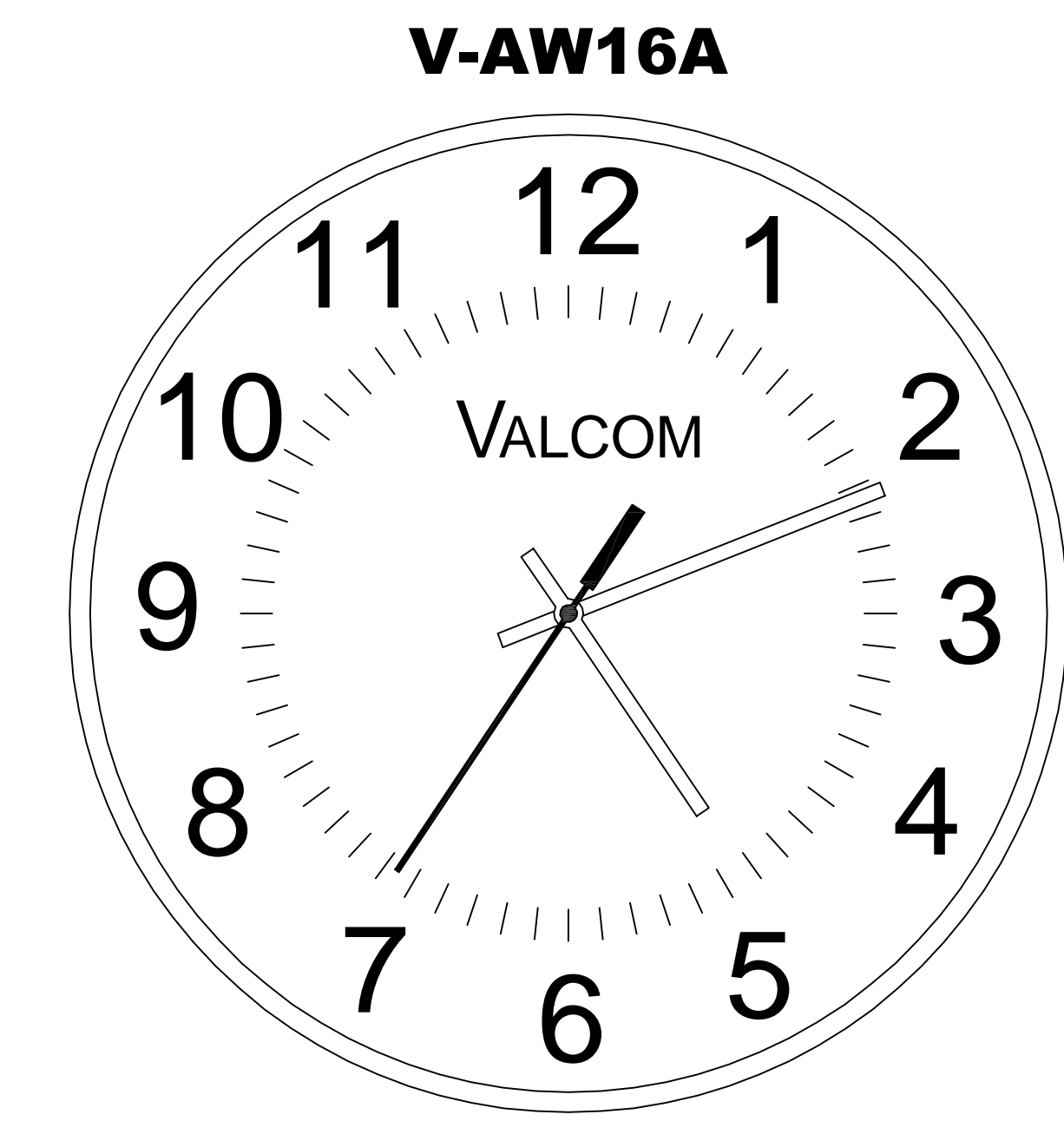
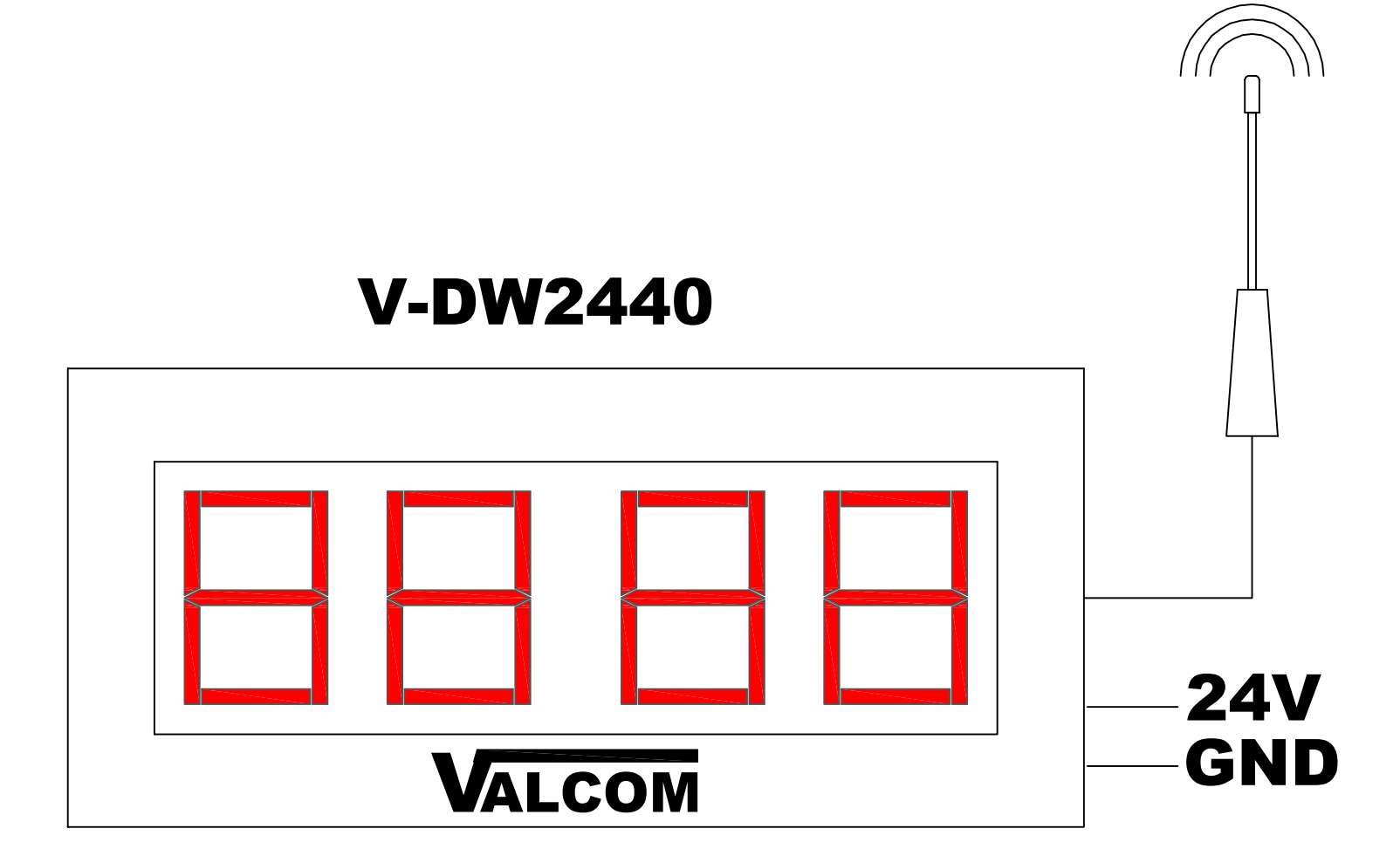
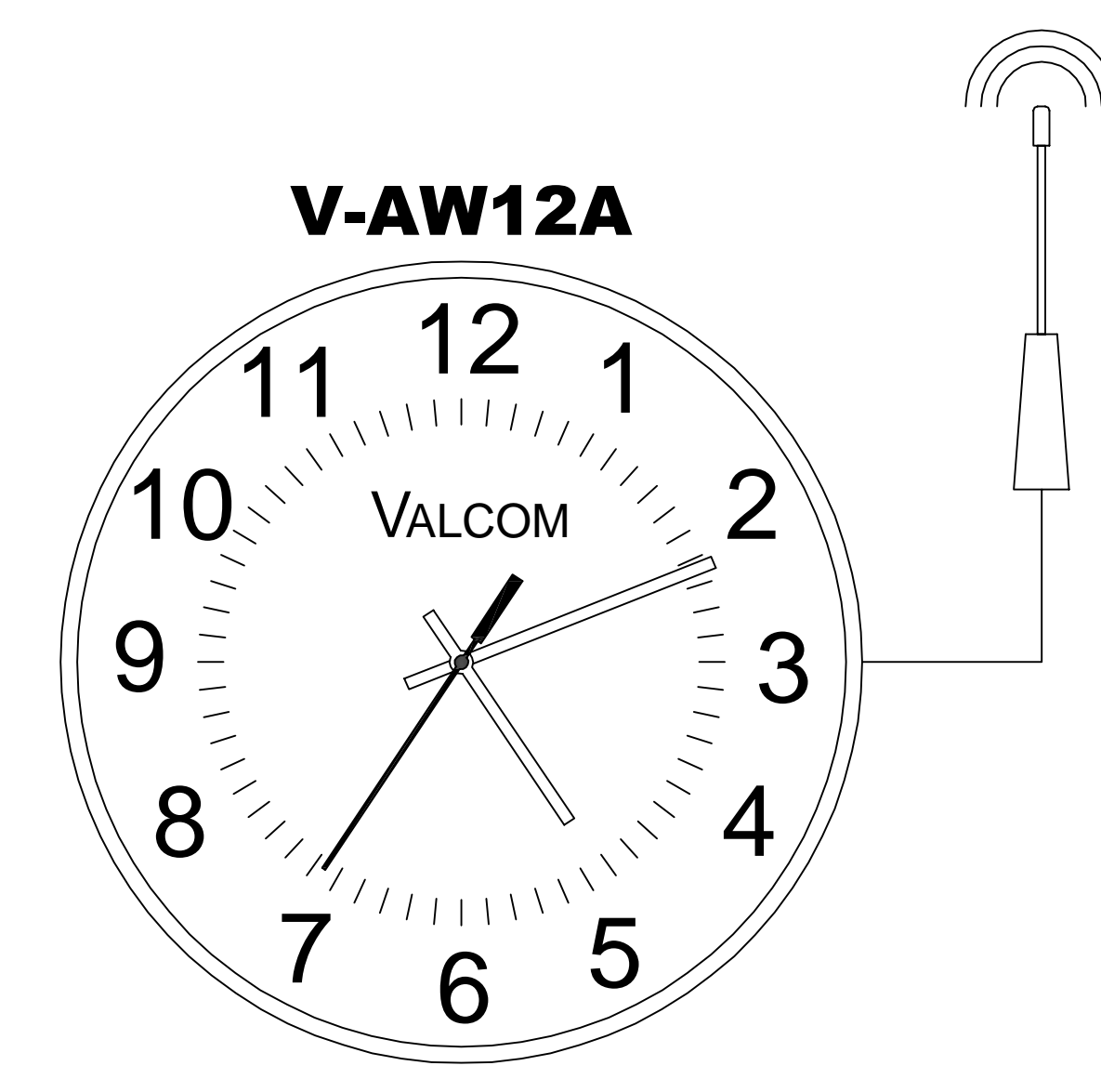


THE V-SER MOUNTS ON THE INTERCOM CPU CARD

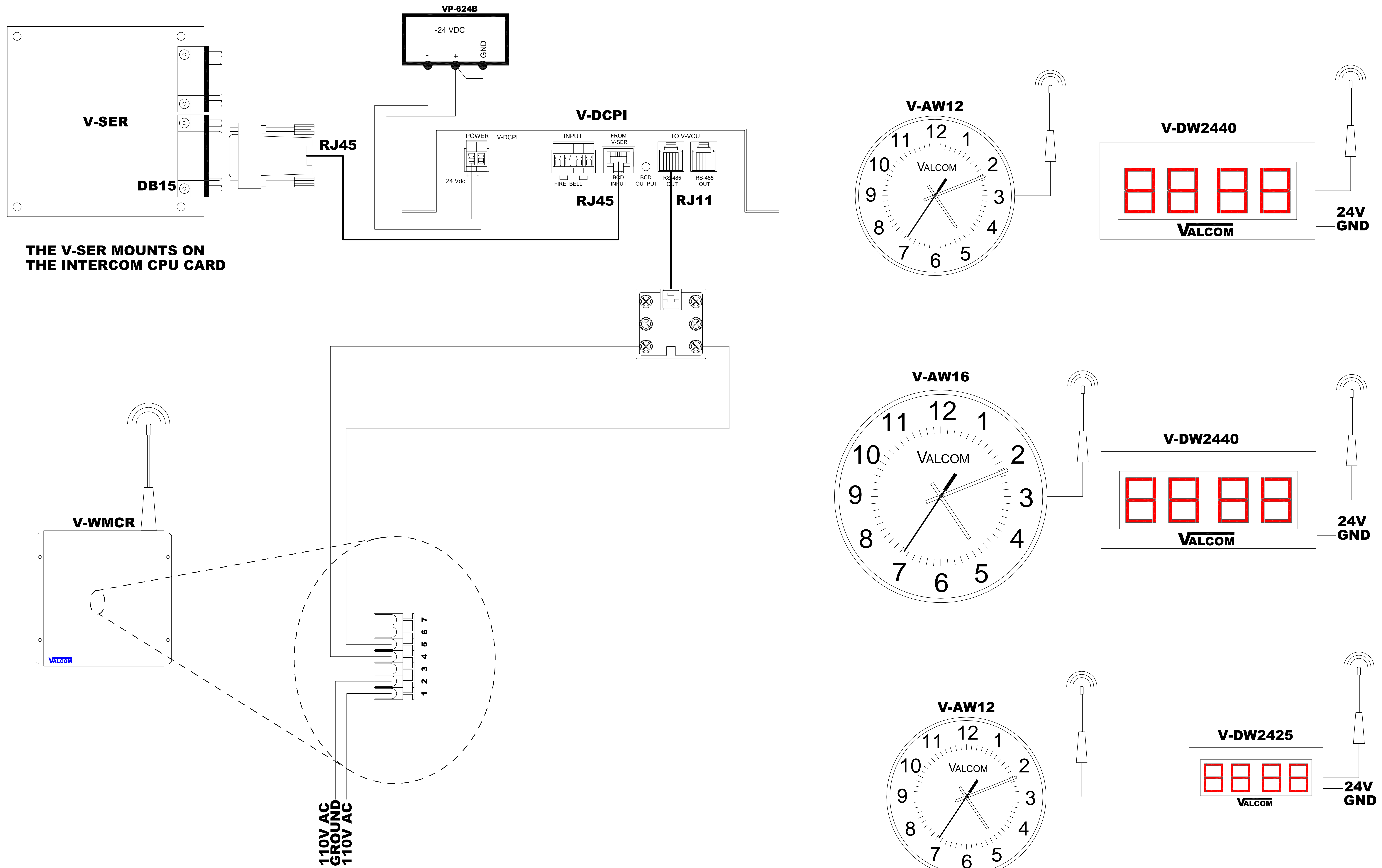


Note: The suggestion provided may or may not be suitable for your intended application. Please consider this information carefully before incorporating it into your system design. Valcom disclaims any responsibility for accuracy or completeness.

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WIRELESS CLOCK HEAD END CLASS CONNECTION OR MULTIPATH CORRECTION SCENARIO

Drawing # 141
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WIRELESS CLOCK HEAD END CLASS CONNECTION OR MULTIPATH CORRECTION SCENARIO

Drawing # 141
Revision A